

Download Timing Verification Of Application Specific Integrated Circuits Asics

Timing Verification of Application-Specific Integrated Circuits (ASICs) Farzad Nekoogar Lecturer, Department of Applied Science, University of California at Davis This book describes the theory and applications of timing verification of application-specific integrated circuits (ASICs). Timing verification is a relatively new concept, which is why most books on digital systems do not cover the issue. This book lays out the fundamental principles of effective timing verification, and it makes good use of the examples that reflect the current issues facing logic designers. Timing Verification of Application Specific Integrated Circuits (ASICs) is a must for all logic designers concerned with the accuracy of timing and clock issues. Autorenkommentar The book was the best seller at the DAC conference, 1999. This book describes the theory and applications of timing verification of application-specific integrated circuits (ASICs). Timing verification is a relatively new concept, which is why most books on digital systems do not cover the issue. This book lays out the fundamental principles of effective timing verification, and it makes good use of the examples that reflect the current issues facing logic designers.